



AGH UNIVERSITY OF SCIENCE
AND TECHNOLOGY



SALT simulations

Faculty of Physics and Applied Computer Science
AGH University of Science and Technology

LHCb UT May 2018



Schematic simulations

Transient

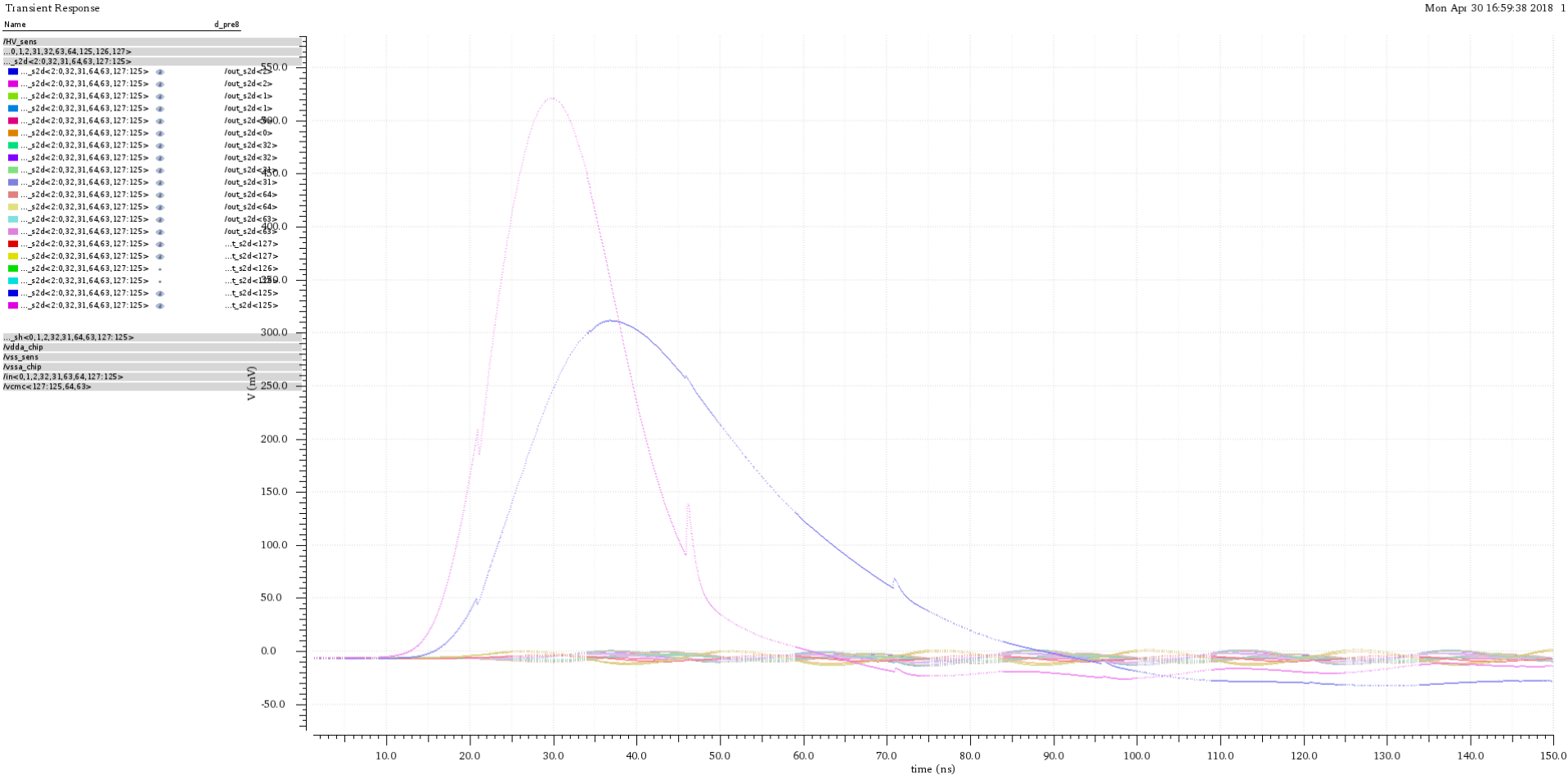
Simulations of SALT 2.5 Schematic plus main resistances

- Simulations of 128 mixed-mode channels (FE+ADC), Digital part not included. Simulator used ++APS(Sampled), conservative
- Simulated configurations of Preamp gnd:
 - Default, with Out_gnd (vssa_pre)
 - With Out_gnd(vssa_pre) and In_gnd(vssai)
 - With In_gnd(vssai)
- In simulations the resistances of power/gnd network are added artificially
- Added resistances:
 - General network (common for all channels, horizontal in layout) - res_vdda_presh=200m, res_vdda_adc=200m, res_vdda_ref=200m, res_vssa_pre=100m, res_vdda_sh=100m, res_vdda_adc=300m)
 - Inter-channel power network (additive to general, in layout vertical going up/down from the middle) - r_vdda=1m, r_vdds2d=8m, r_vddadc=3m, r_vrefd=14.5m, r_vssai=2m, r_vssa=2m, r_vsss2d=8m, r_vssadc=3m)



Schematic Simulations of SALT 2.5 S2d out (Only Out_gnd, Cdet 12pF, Ipre 4/15)

Mon Apr 30 16:59:38 2018 1

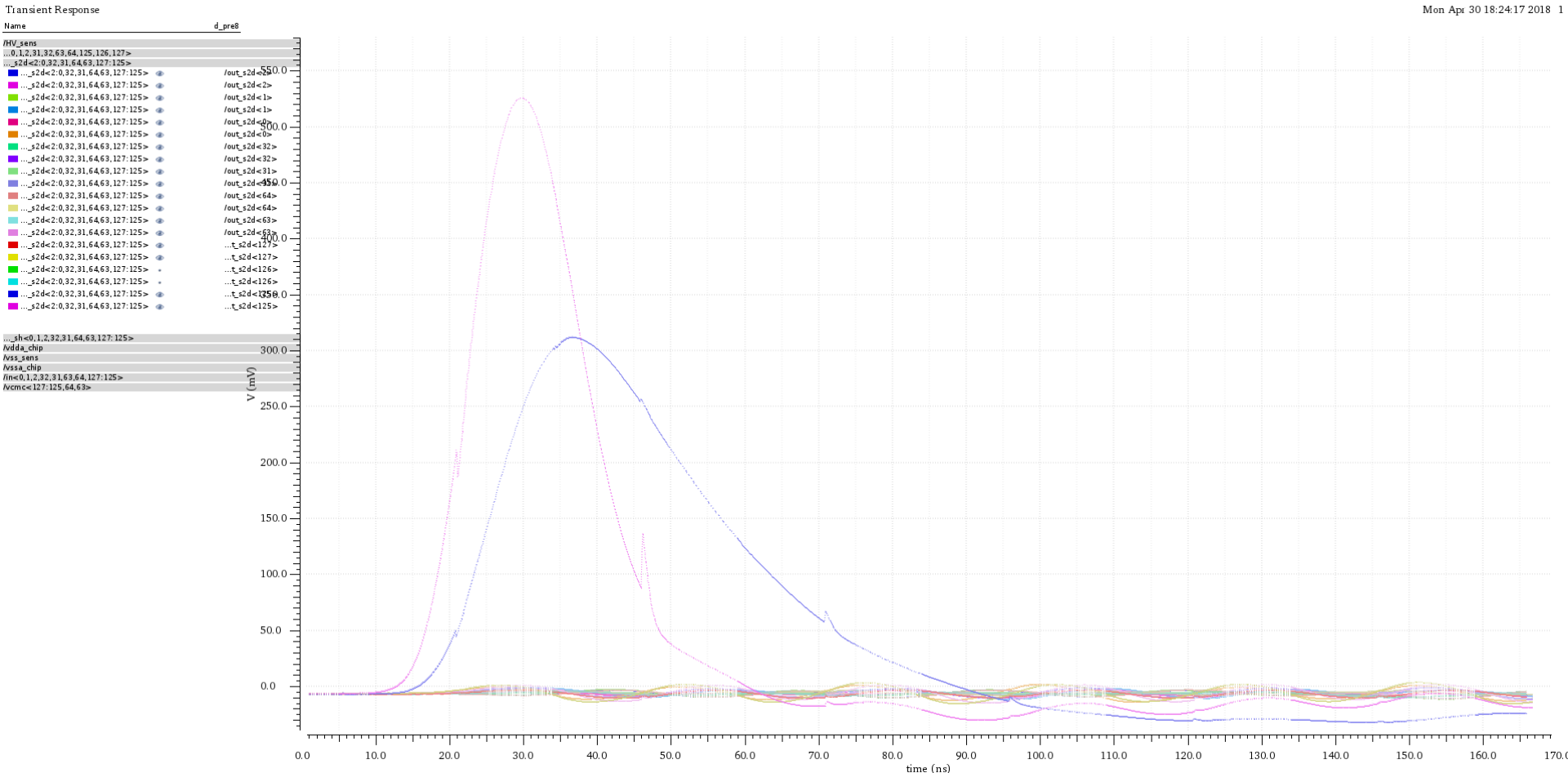


- Two pulses for Ipre=4/15
- Other channels with baseline oscillations within ~1LSB



Schematic Simulations of SALT 2.5 S2d out (Out_gnd&In_gnd, Cdet 12pF, Ipre 4/15)

Mon Apr 30 18:24:17 2018 1



- Two pulses for $I_{pre}=4/15$
- Other channels with baseline oscillations within $\sim 1\text{LSB}$



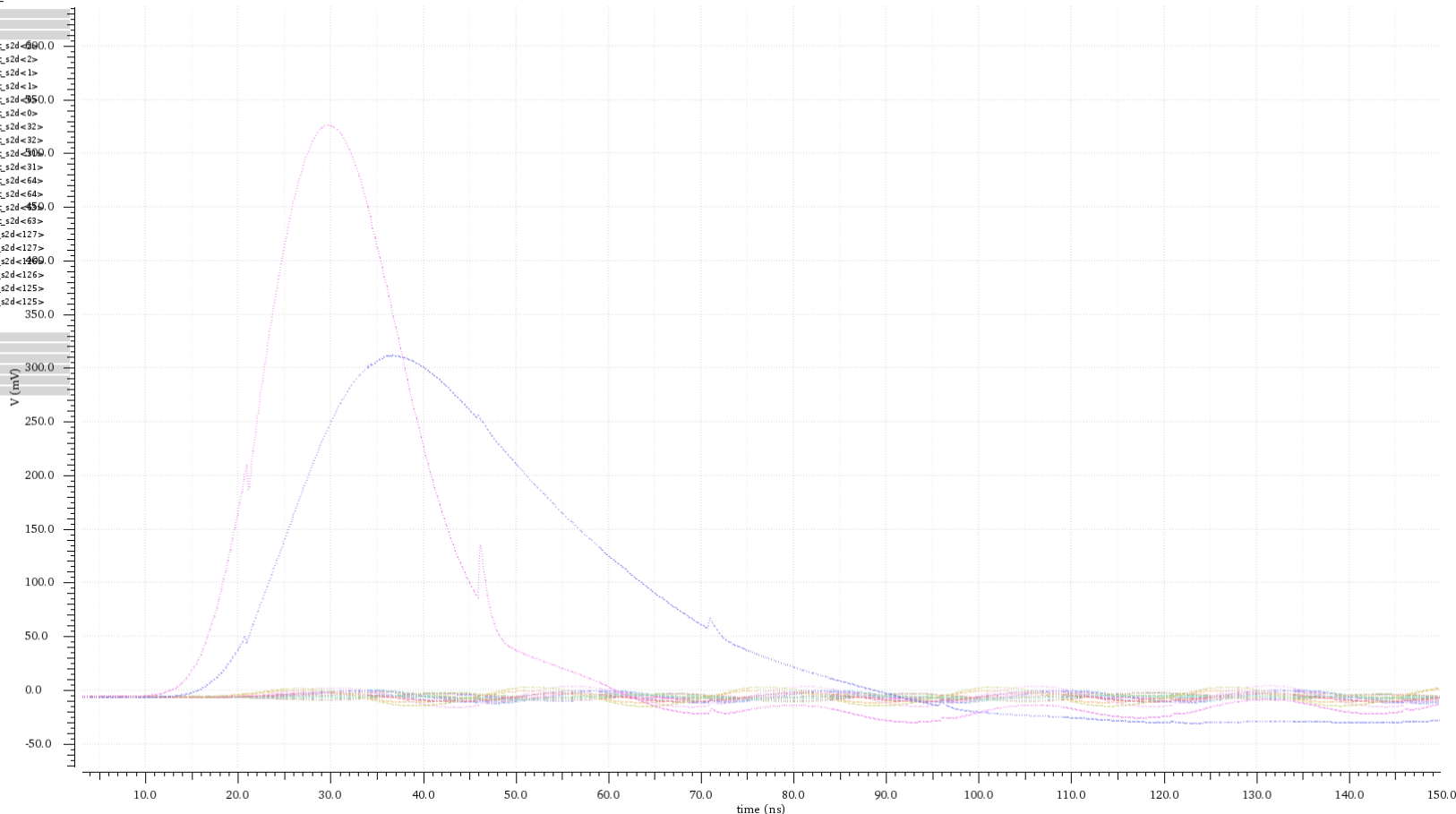
Schematic Simulations of SALT 2.5 S2d out (Only In_gnd, Cdet 12pF, Ipre 4/15)

Transient Response

Wed May 2 10:35:55 2018 1

Name d_pre8

Name	d_pre8
/HV_sens	
..._s2d<2:0.32,31.64,63,127:125>	
..._s2d<2:0.32,31.64,63,127:125>	/out_s2d<00>
..._s2d<2:0.32,31.64,63,127:125>	/out_s2d<2>
..._s2d<2:0.32,31.64,63,127:125>	/out_s2d<1>
..._s2d<2:0.32,31.64,63,127:125>	/out_s2d<1>
..._s2d<2:0.32,31.64,63,127:125>	/out_s2d<050>
..._s2d<2:0.32,31.64,63,127:125>	/out_s2d<0>
..._s2d<2:0.32,31.64,63,127:125>	/out_s2d<32>
..._s2d<2:0.32,31.64,63,127:125>	/out_s2d<32>
..._s2d<2:0.32,31.64,63,127:125>	/out_s2d<300>
..._s2d<2:0.32,31.64,63,127:125>	/out_s2d<31>
..._s2d<2:0.32,31.64,63,127:125>	/out_s2d<64>
..._s2d<2:0.32,31.64,63,127:125>	/out_s2d<64>
..._s2d<2:0.32,31.64,63,127:125>	/out_s2d<450>
..._s2d<2:0.32,31.64,63,127:125>	/out_s2d<63>
..._s2d<2:0.32,31.64,63,127:125>	..._t_s2d<127>
..._s2d<2:0.32,31.64,63,127:125>	..._t_s2d<127>
..._s2d<2:0.32,31.64,63,127:125>	..._t_s2d<100>
..._s2d<2:0.32,31.64,63,127:125>	..._t_s2d<126>
..._s2d<2:0.32,31.64,63,127:125>	..._t_s2d<125>
..._s2d<2:0.32,31.64,63,127:125>	..._t_s2d<125>
..._sh=0.1,2.32,31.64,63,127:125>	
/vdd_a_chip	
/vss_sens	
/vss_a_chip	
/in<0.1,2.32,31.63,64,127:125>	
/vcm<127:125,64,63>	



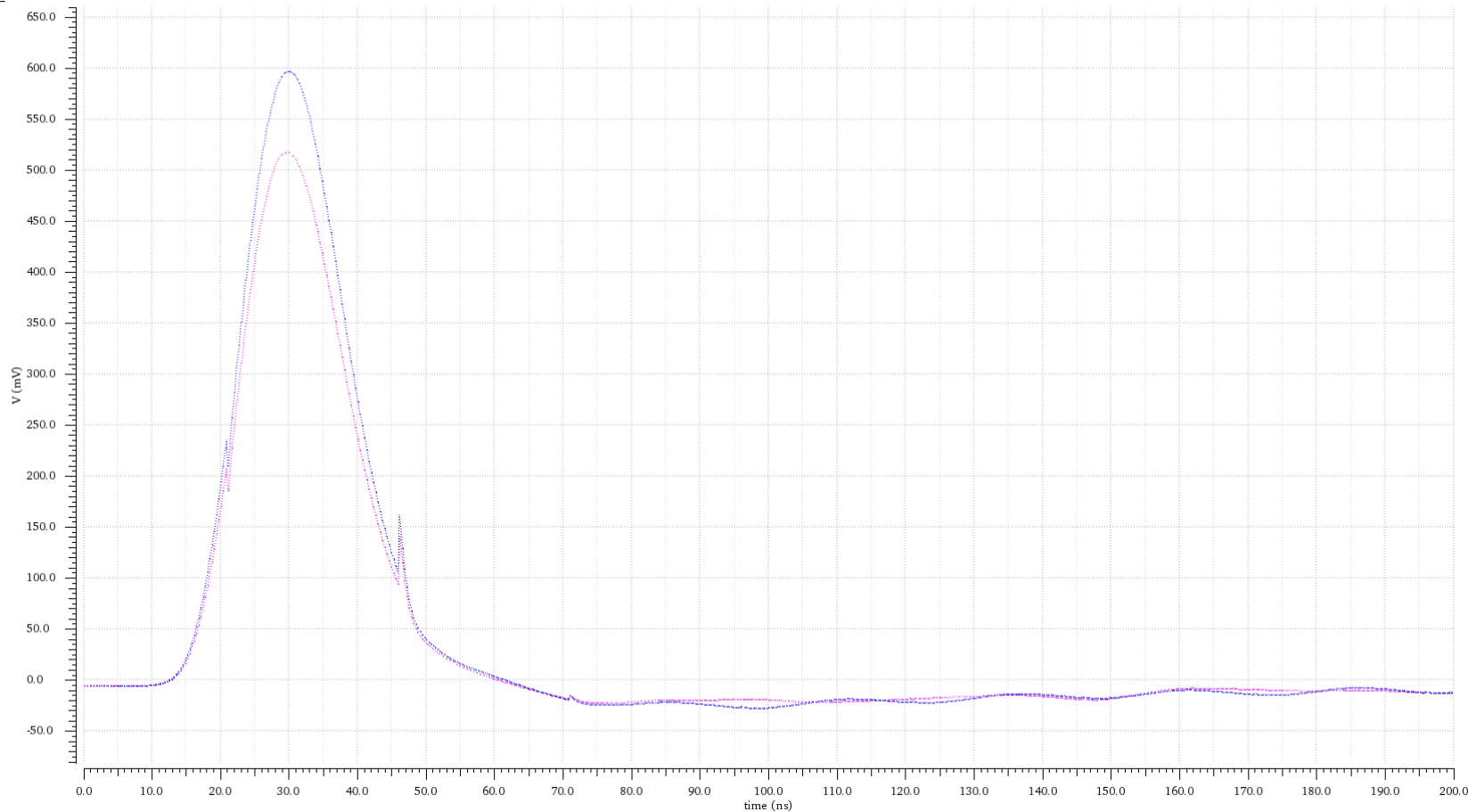
- Two pulses for $I_{pre}=4/15$
- Other channels with baseline oscillations within $\sim 1\text{LSB}$



Schematic Simulations of SALT 2.5 S2d out (Only Out_gnd, Cdet 12pF, Ipre 15) Comparison: ++APS_Sampled vs APS

Transient Analysis 'tran' time = (0 s -> 600 ns)

Name	bit
...d<2:0.32.31.64.63.127:125>	...s2d<125> 1
...d<2:0.32.31.64.63.127:125>	...s2d<125> 1



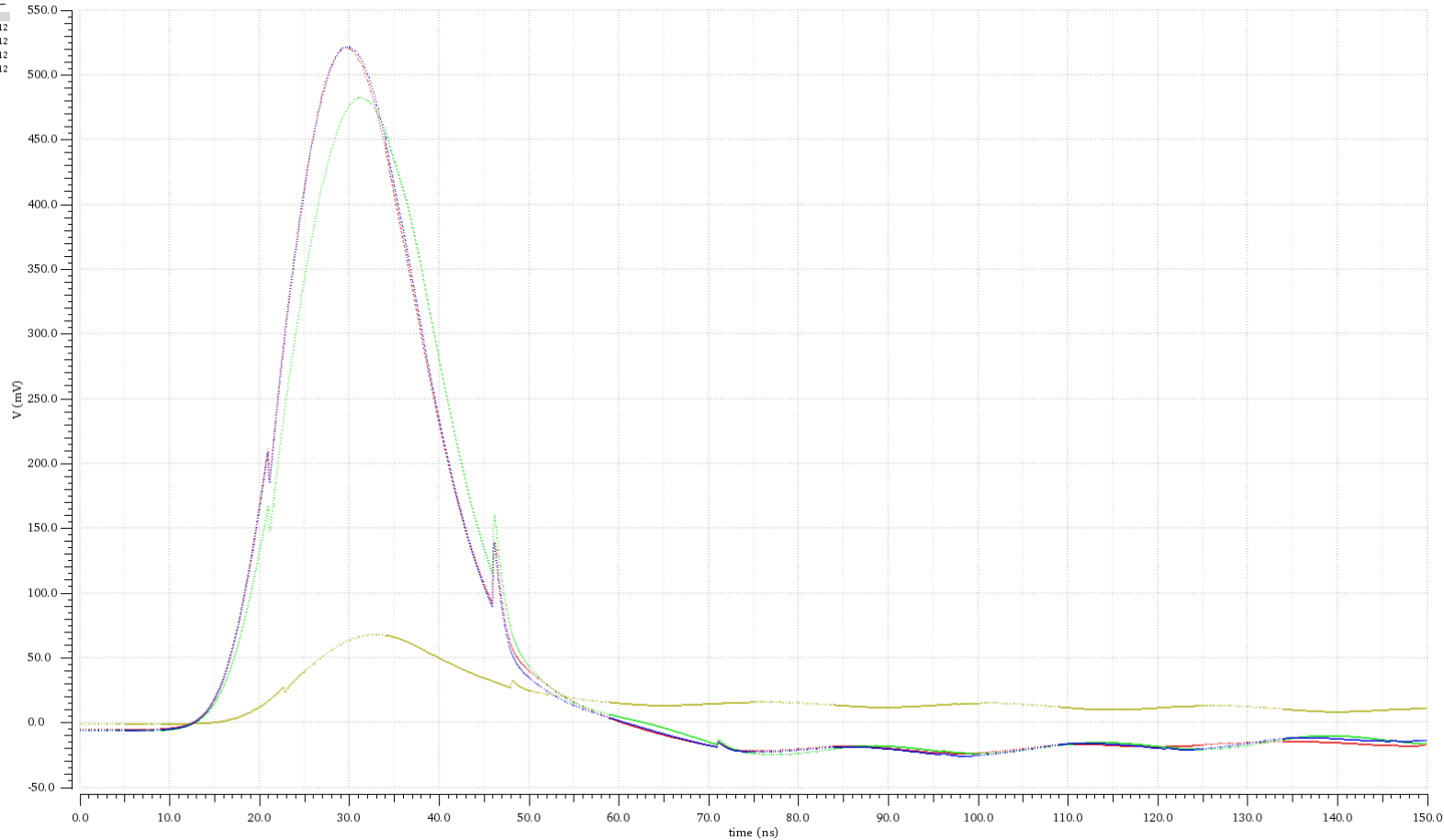
- Blue with APS, Pink with ++APS_Sampled
- Visible difference in pulse amplitude but not in baseline oscillations ₇



Schematic Simulations of SALT 2.5 S2d out (Only Out_gnd, Cdet 12pF) vcm = 0,16,32,63

Transient Analysis 'tran' time = (0 s -> 150 ns)

Name	bit	...
...s2d<2:0.32,31.64,63,127:125>	...	12
...s2d<2:0.32,31.64,63,127:125>	...	12
...s2d<2:0.32,31.64,63,127:125>	...	12
...s2d<2:0.32,31.64,63,127:125>	...	0
...s2d<2:0.32,31.64,63,127:125>	...	12

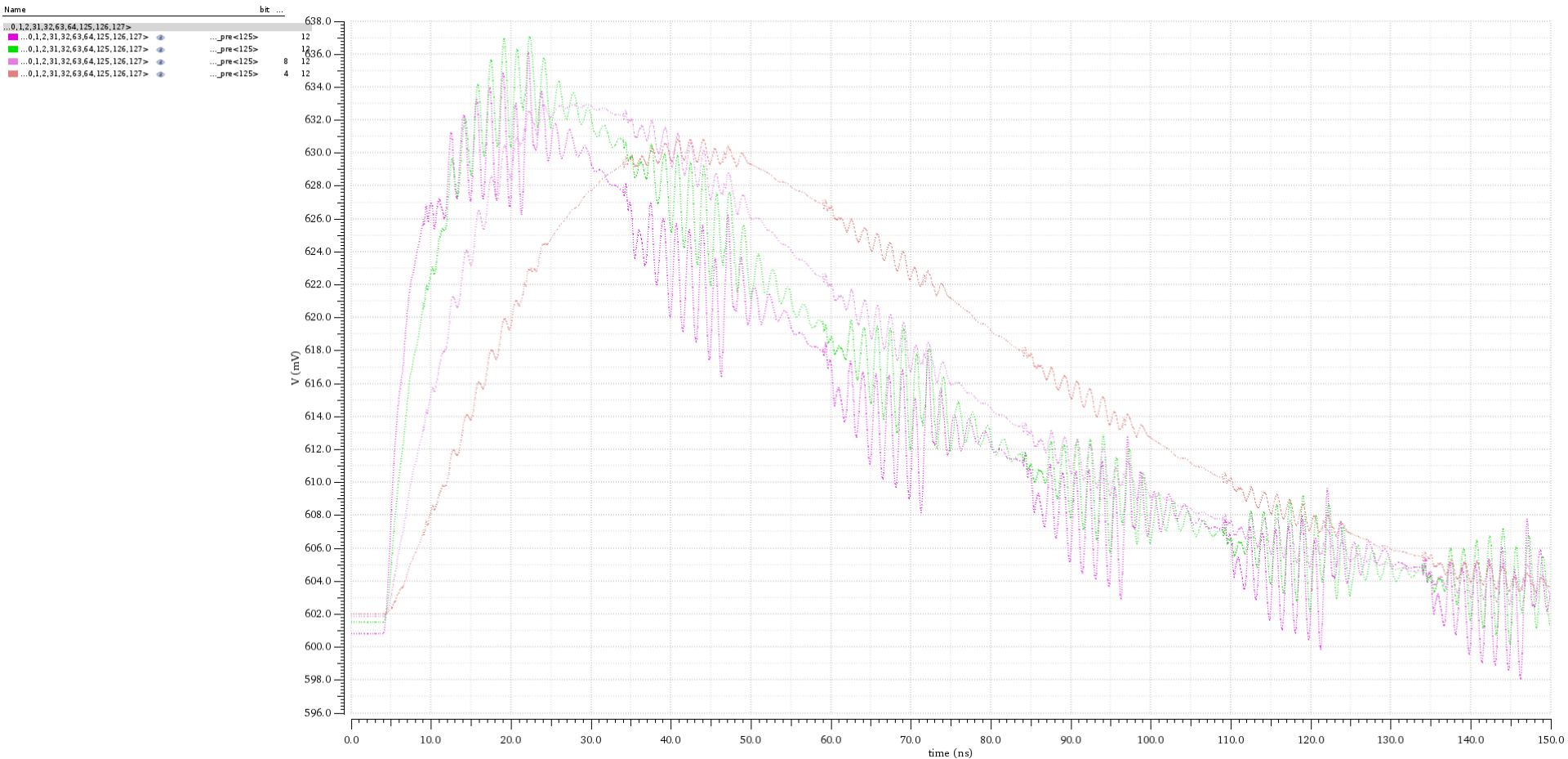


- For 16,32 ok, lower for 63, very low for 0



Schematic Simulations of SALT 2.5 Preamp out (Only Out_gnd, Cdet 12pF) $I_{pre} = 4, 8, 15, 30$

Transient Analysis 'tran' time = (0 s -> 150 ns)



- Faster pulse for higher I_{pre}
- Baseline DC change of 1-2mV



Simulations of SALT 2.5

Schematic plus main resistances

DC measurements

- Default settings are:
 - $I_{pre}=15$
 - $I_{krum}=4$
 - $I_{vcm}=32$
 - $I_{sh}=12$
 - $I_{s2d}=12$
 - $I_{base}=128$
 - $I_{pulse}=15$
- ..



Schematic Simulations of SALT 2.5 Ivdda (Only Out_gnd, Cdet 12pF) Ipre=4,8,15,30, other settings default

Transient Analysis 'tran' time = (0 s -> 150 ns)

Name	...	8
/I156/VDDA		
/I156/VDDA	30	12
/I156/VDDA	15	12
/I156/VDDA	8	12
/I156/VDDA	4	12

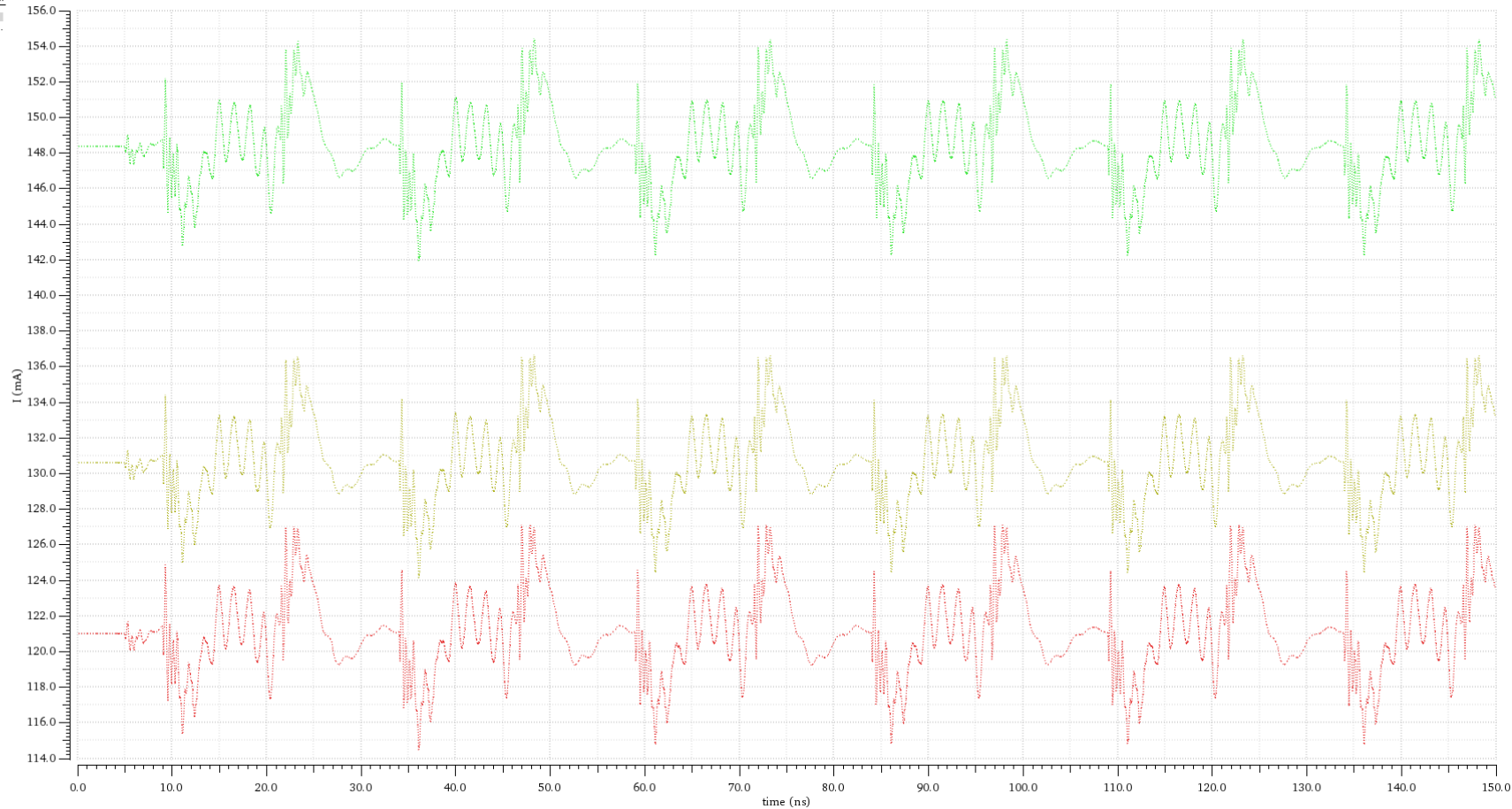




Schematic Simulations of SALT 2.5 Ivdda (Only Out_gnd, Cdet 12pF) Ish=3,6,12, other settings default

Transient Analysis 'tran' time = (0 s -> 150 ns)

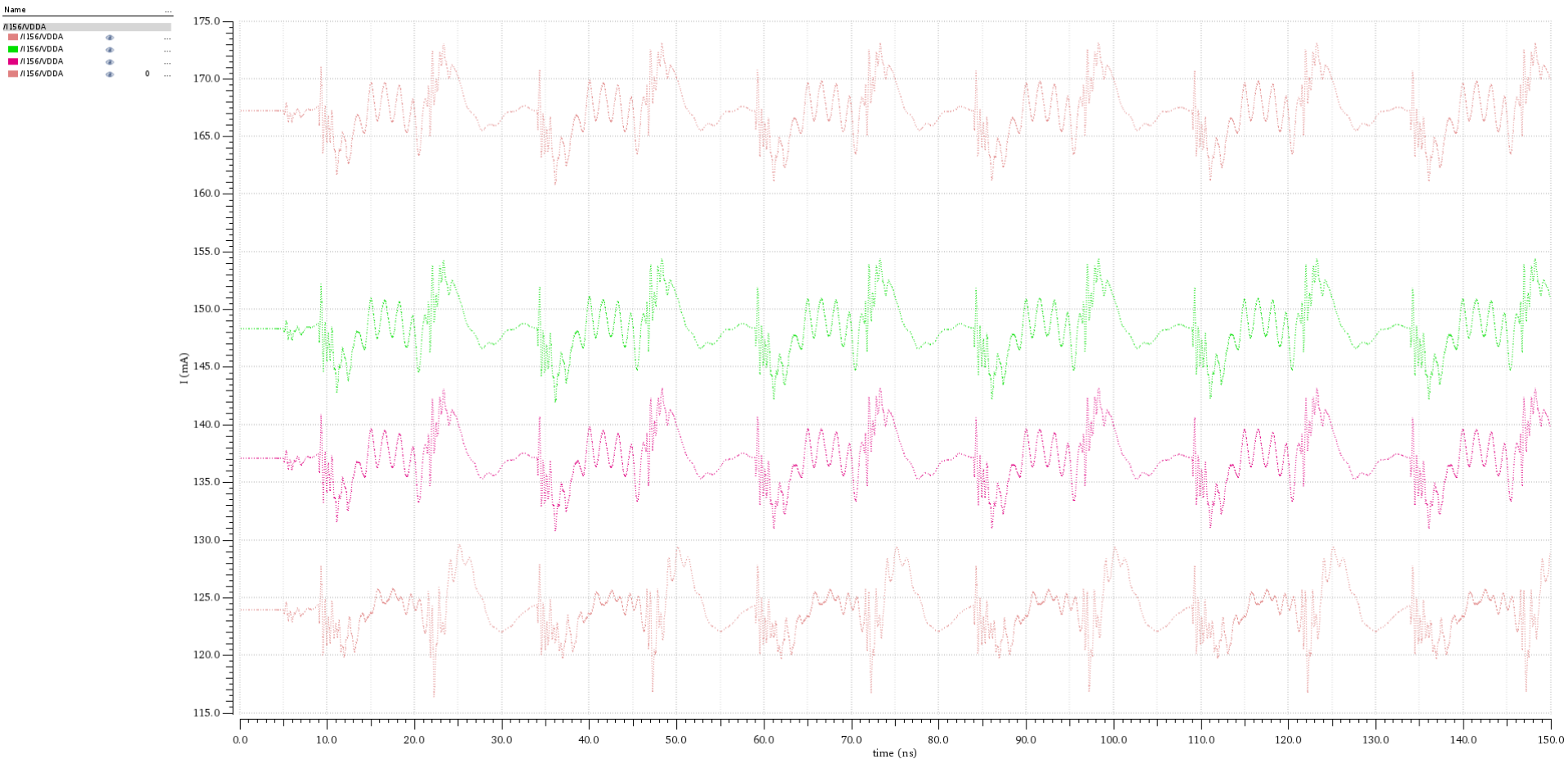
Name	Color	Value
/I156/VDDA	Green	6
/I156/VDDA	Yellow	6
/I156/VDDA	Red	3





Schematic Simulations of SALT 2.5 Ivdda (Only Out_gnd, Cdet 12pF) Ivcm=0,16,32,63, other settings default

Transient Analysis 'tran' time = (0 s -> 150 ns)



- Highest current for highest (63) setting

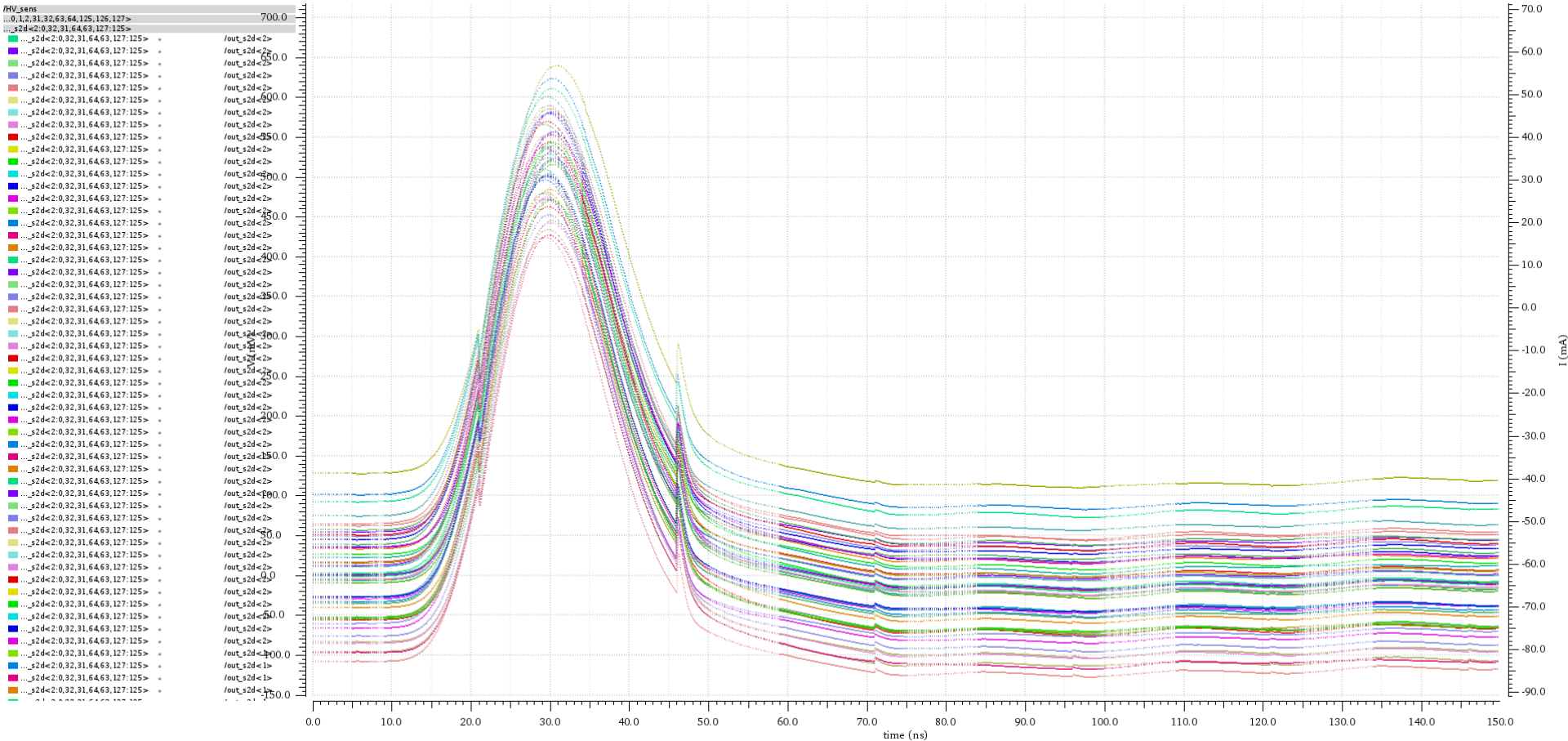


Schematic Simulations of SALT 2.5 S2d out MC (Cdet 12pF, Ipre 15)

Transient Response

Sun May 6 15:48:26 2018 1

Name mcpamset



- The S2D(differentially) DC baseline stays in the range ± 130 mV

Simulations of SALT 2.5

Effects of internal inductances and their couplings

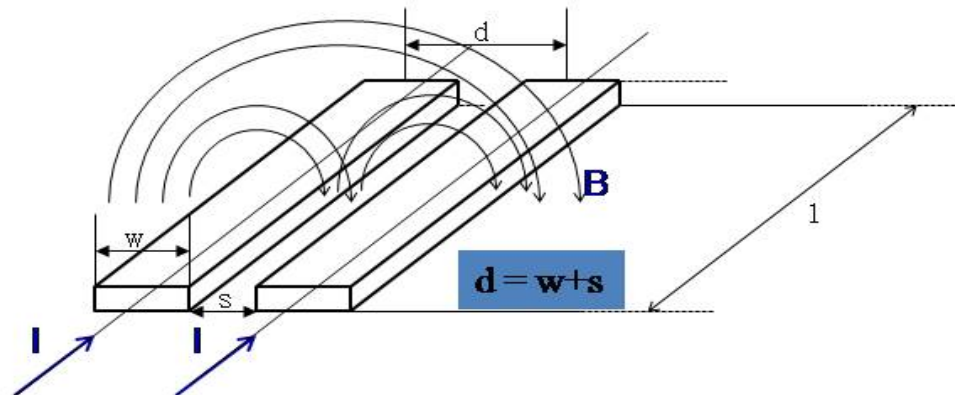
- The main effect of internal inductances may come from long ($\sim 2.5\text{mm}$) horizontal lines of ADC power supply and Preamp ground which are placed in parallel, have widths of $100\text{-}200\mu\text{m}$ and are separated by less than $50\mu\text{m}$.
- First order estimation of the inductance of ADC power or Preamp gnd line is about $1.5\text{-}1.7\text{nH}$ for each of them.
- First order estimation of coupling between ADC power and Preamp gnd lines is huge, of about 50%. The estimation is based on the figure/equation from next slide. We suspect that in reality, because there are many other metal lines around, it is significantly less.

Simulations of SALT 2.5

Effects of internal inductances and their couplings

Mutual Inductance between Lines

Since some of the flux generated in one line is “coupled” to the adjacent line, “mutual” inductance is introduced between the two lines.

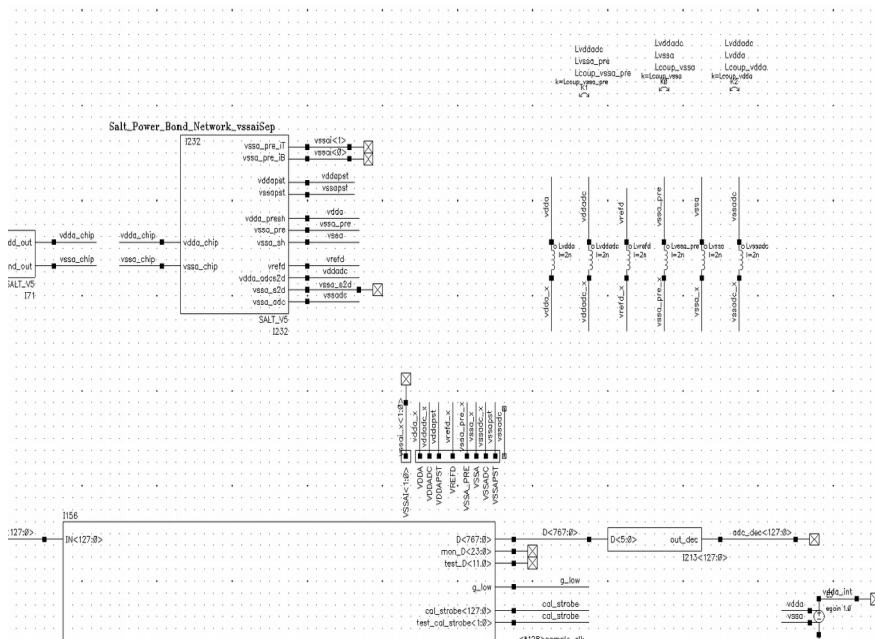


$$M = \frac{\mu_0}{2\pi} l \left(\ln \left(\sqrt{1 + \left(\frac{l}{d}\right)^2} + \frac{l}{d} \right) - \sqrt{1 + \left(\frac{d}{l}\right)^2} + \frac{d}{l} \right) [H] \quad \text{Dimensions in m}$$

Mutual inductance can be positive or negative depending on current direction.

Schematic Simulations of SALT 2.5

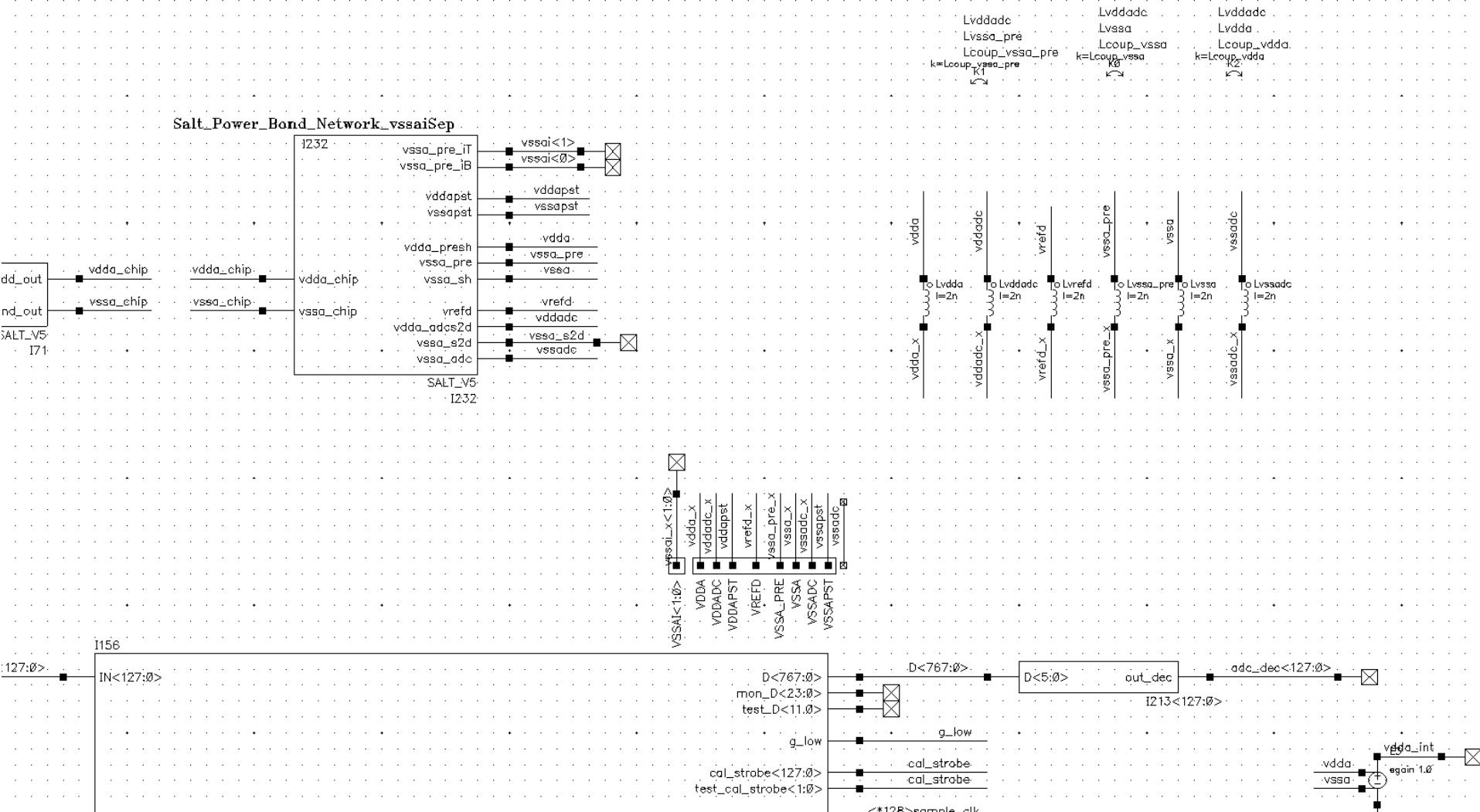
How inductances and their couplings are simulated



- Top left (Salt_Power_Bond...) block contains bond inductances and series resistances of long power/gnd lines in SALT2.5.
- Bottom - SALT2.5 (only the side with power supply network is seen)
- Inductances Lvdda, Lvddadc, ..., simulate internal inductances (e.g. horizontal power/gnd lines). For 1st appr. 2nH was used.
- Top right - Lcouplings. 3 are seen here: vddadc-vssa_pre, vddadc-vssa, vddadc-vdda. Each of them may be set between -1,+1. For 1st simulation only vddadc-vssa_pre was used.
- Next slide shows the zoom of this schematic...

Schematic Simulations of SALT 2.5

How inductances and their couplings are simulated



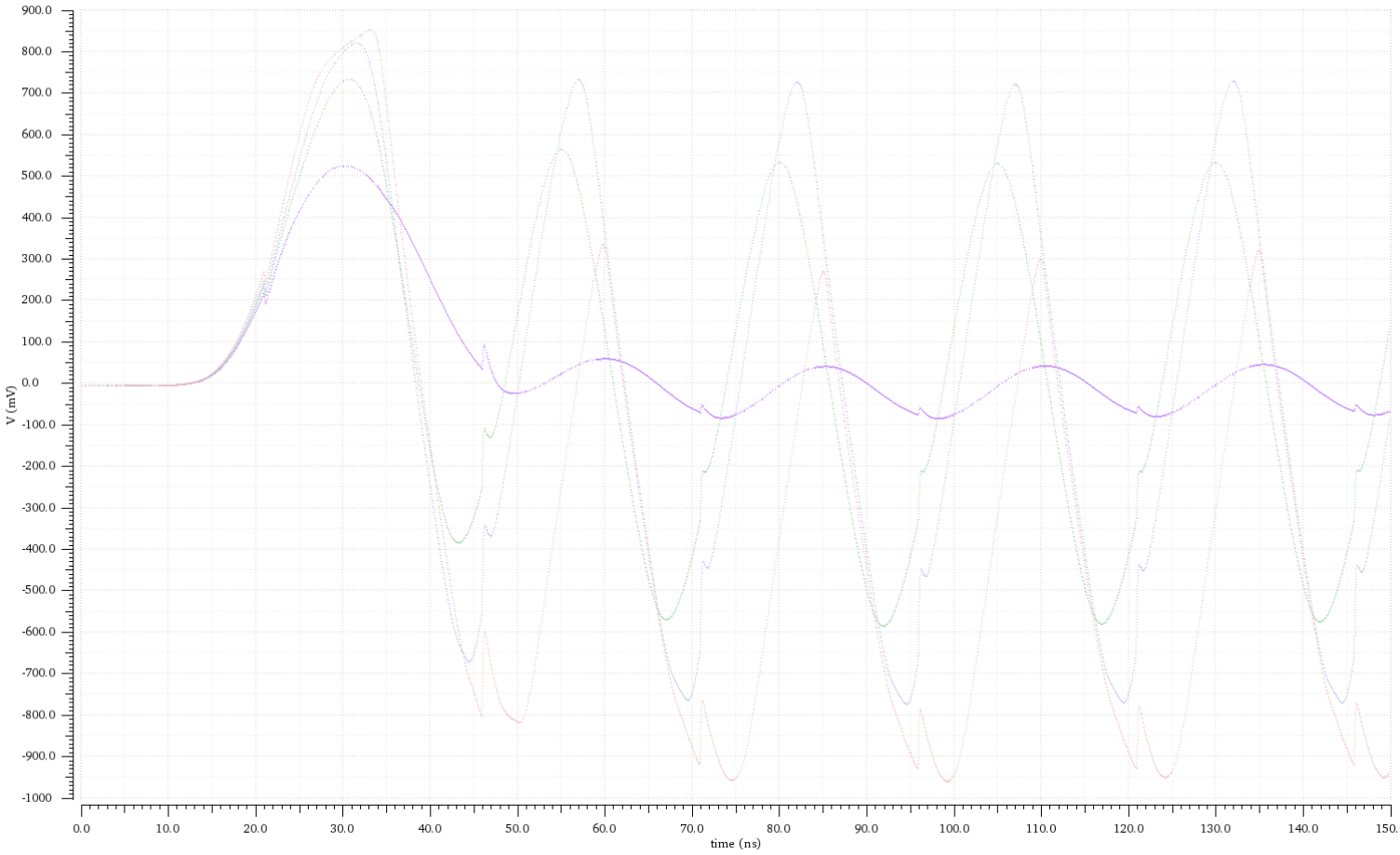


Schematic Simulations of SALT 2.5 S2Diff out vs Lcoup (Vdd_ADC, Vssa_Pre) (Only Out_gnd, Cdet 12pF)

Thu May 31 10:20:46 2018

Transient Response

Name	bit...	adc	...	ssa
/out_s2d<=9493.64.60>				
/out_s2d<=2.0.32.31.64.63.127.125>				
/out_s2d<=2.0.32.31.64.63.127.125>	<=2>	1	...	11 0.1
/out_s2d<=2.0.32.31.64.63.127.125>	<=2>	1	...	11 0.05
/out_s2d<=2.0.32.31.64.63.127.125>	<=2>	1	...	11 0.03
/out_s2d<=2.0.32.31.64.63.127.125>	<=2>	1	...	11 0
/out_s2d<=2.0.32.31.64.63.127.125>	<=2>	1	...	11 ...03
/out_s2d<=2.0.32.31.64.63.127.125>	<=2>	1	...	11 ...05
/out_s2d<=2.0.32.31.64.63.127.125>	<=2>	1	...	11 ...01
/out_s2d<=2.0.32.31.64.63.127.125>	<=2>	1	2e-12	0.1
/out_s2d<=2.0.32.31.64.63.127.125>	<=2>	1	2e-12	0.05
/out_s2d<=2.0.32.31.64.63.127.125>	<=2>	1	2e-12	0.03
/out_s2d<=2.0.32.31.64.63.127.125>	<=2>	1	2e-12	0
/out_s2d<=2.0.32.31.64.63.127.125>	<=2>	1	2e-12	...03
/out_s2d<=2.0.32.31.64.63.127.125>	<=2>	1	2e-12	...05
/out_s2d<=2.0.32.31.64.63.127.125>	<=2>	1	2e-12	...01
/out_s2d<=2.0.32.31.64.63.127.125>	<=2>	0.2	...	11 0.1
/out_s2d<=2.0.32.31.64.63.127.125>	<=2>	0.2	...	11 0.05
/out_s2d<=2.0.32.31.64.63.127.125>	<=2>	0.2	...	11 0.03
/out_s2d<=2.0.32.31.64.63.127.125>	<=2>	0.2	...	11 0
/out_s2d<=2.0.32.31.64.63.127.125>	<=2>	0.2	...	11 ...03
/out_s2d<=2.0.32.31.64.63.127.125>	<=2>	0.2	...	11 ...05
/out_s2d<=2.0.32.31.64.63.127.125>	<=2>	0.2	...	11 ...01
/out_s2d<=2.0.32.31.64.63.127.125>	<=1b>	1	...	11 0.1
/out_s2d<=2.0.32.31.64.63.127.125>	<=1b>	1	...	11 0.05
/out_s2d<=2.0.32.31.64.63.127.125>	<=1b>	1	...	11 0.03
/out_s2d<=2.0.32.31.64.63.127.125>	<=1b>	1	...	11 0
/out_s2d<=2.0.32.31.64.63.127.125>	<=1b>	1	...	11 ...03
/out_s2d<=2.0.32.31.64.63.127.125>	<=1b>	1	...	11 ...05
/out_s2d<=2.0.32.31.64.63.127.125>	<=1b>	1	...	11 ...01
/out_s2d<=2.0.32.31.64.63.127.125>	<=1b>	1	2e-12	0.1
/out_s2d<=2.0.32.31.64.63.127.125>	<=1b>	1	2e-12	0.05
/out_s2d<=2.0.32.31.64.63.127.125>	<=1b>	1	2e-12	0.03
/out_s2d<=2.0.32.31.64.63.127.125>	<=1b>	1	2e-12	0
/out_s2d<=2.0.32.31.64.63.127.125>	<=1b>	1	2e-12	...03
/out_s2d<=2.0.32.31.64.63.127.125>	<=1b>	1	2e-12	...05
/out_s2d<=2.0.32.31.64.63.127.125>	<=1b>	1	2e-12	...01
/out_s2d<=2.0.32.31.64.63.127.125>	<=1b>	0.2	...	11 0.1
/out_s2d<=2.0.32.31.64.63.127.125>	<=1b>	0.2	...	11 0.05
/out_s2d<=2.0.32.31.64.63.127.125>	<=1b>	0.2	...	11 0.03
/out_s2d<=2.0.32.31.64.63.127.125>	<=1b>	0.2	...	11 0
/out_s2d<=2.0.32.31.64.63.127.125>	<=1b>	0.2	...	11 ...03
/out_s2d<=2.0.32.31.64.63.127.125>	<=1b>	0.2	...	11 ...05
/out_s2d<=2.0.32.31.64.63.127.125>	<=1b>	0.2	...	11 ...01



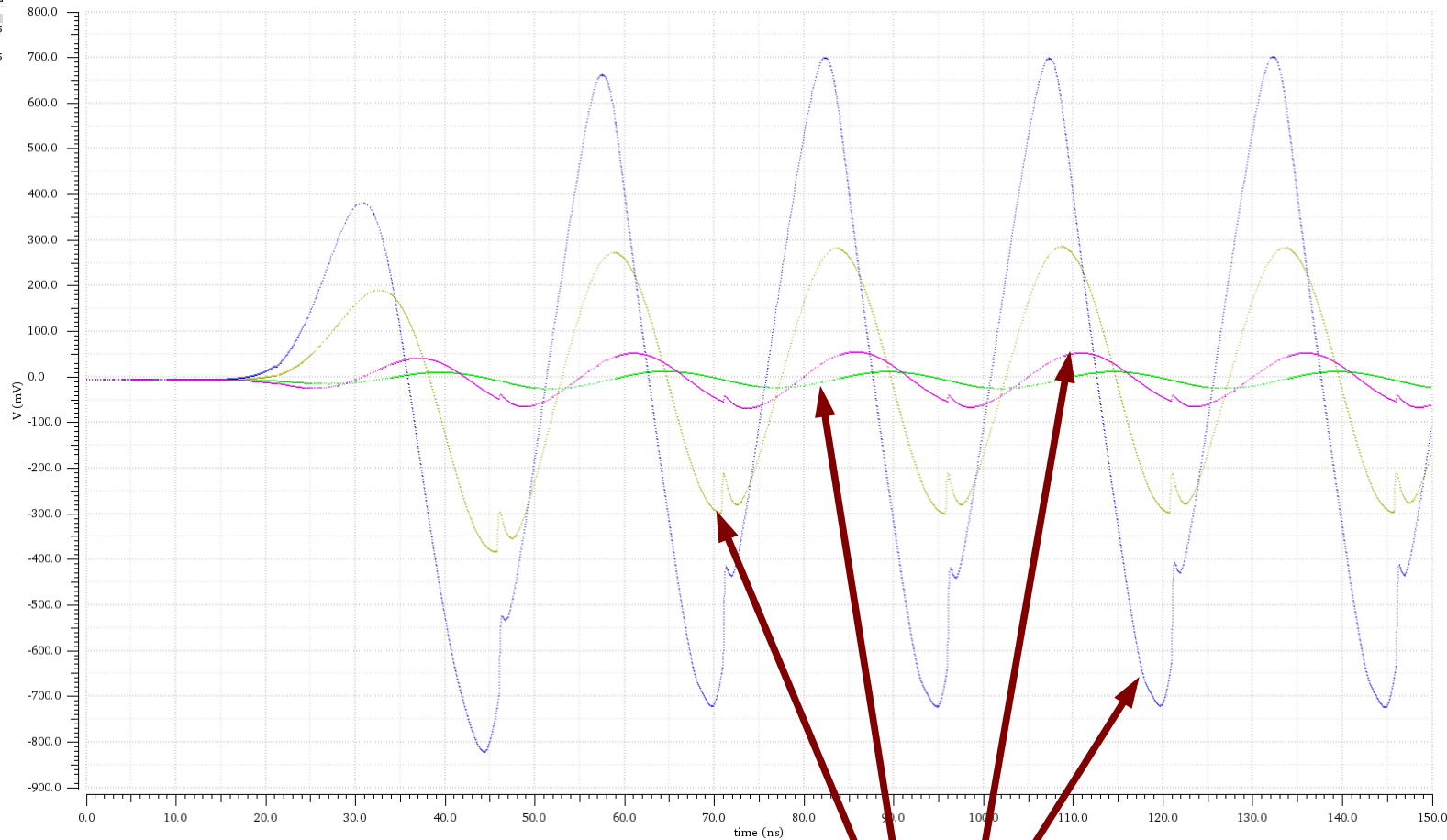
- Internal inductance of 2nH for Vdd_ADC and Vssa_Pre added
- Lcoupling between Vdd_ADC and Vssa_Pre -0.1, -0.05, -0.03, 0.0



Schematic Simulations of SALT 2.5 S2Diff out vs Lcoup (Vdd_ADC, Vssa_Pre) vs Ipre (Only Out_gnd, Cdet 12pF)

Transient Analysis 'tran' time = (0 s -> 150 ns)

Name	bit	...	sa
/out_s2d<94.93.64.60>			
/out_s2d<94.93.64.60>	4	-0.05	
/out_s2d<94.93.64.60>	4	0	
/out_s2d<94.93.64.60>	-0.05		
/out_s2d<94.93.64.60>	0		



- Internal inductance of 2nH for Vdd_ADC and Vssa_Pre added
- Lcoupling Vdd_ADC and Vssa_Pre -0.05, 0.0, Ipre 4, 15
- Ipre has big effect on oscillations amplitude!



Simulations of SALT 2.5

Effects of internal inductances and their couplings

- In addition to previous simulations, the 2nH internal inductance has been added to all power/gnd lines: Vdda_ADC, Vrefd, Vssa_adc, Vdda, Vssa, Vssa_Pre
- Different coupling configurations will be studied...



Schematic Simulations of SALT 2.5 S2Diff out (Lcoup -0.1,0, Cdet 12pF) (Vdd_ADC coupled to Vssa_Pre or Vssa)

Transient Analysis 'tran': time = (0 s -> 150 ns)

Name	bit...re
/out_s2d<94,93,64:60>	...s2d<60> ... 0
/out_s2d<94,93,64:60>	...s2d<60> 0 -0.1
/out_s2d<94,93,64:60>	...s2d<60> 0 0



- Lcoupling to: Vssa_Pre, Vssa, No coupling
- Coupling to Vssa_pre is dominant!

Simulations of SALT 2.5

Effects of internal inductances and their couplings

- An attempt to estimate the effect of inductive coupling between vertical power/gnd lines was made...
- The same as before 2nH inductances were used for vertical lines, although vertical length is about 1cm (4 x more than horizontal). On the other hand not whole ADC current flows through the whole length...
- In the layout the lines (from the left) go in order: Vssa_pre, Vdda, Vssa, Vdda, ADC power and gnd lines
- Since the dominant effect comes from Vssa_pre sensitivity, it was assumed that Vdda_ADC couples to Vdda and then Vdda to Vssa_pre. The same coupling strength for both pairs was used.
- In these simulations horizontal coupling/inductances were OFF (zero).



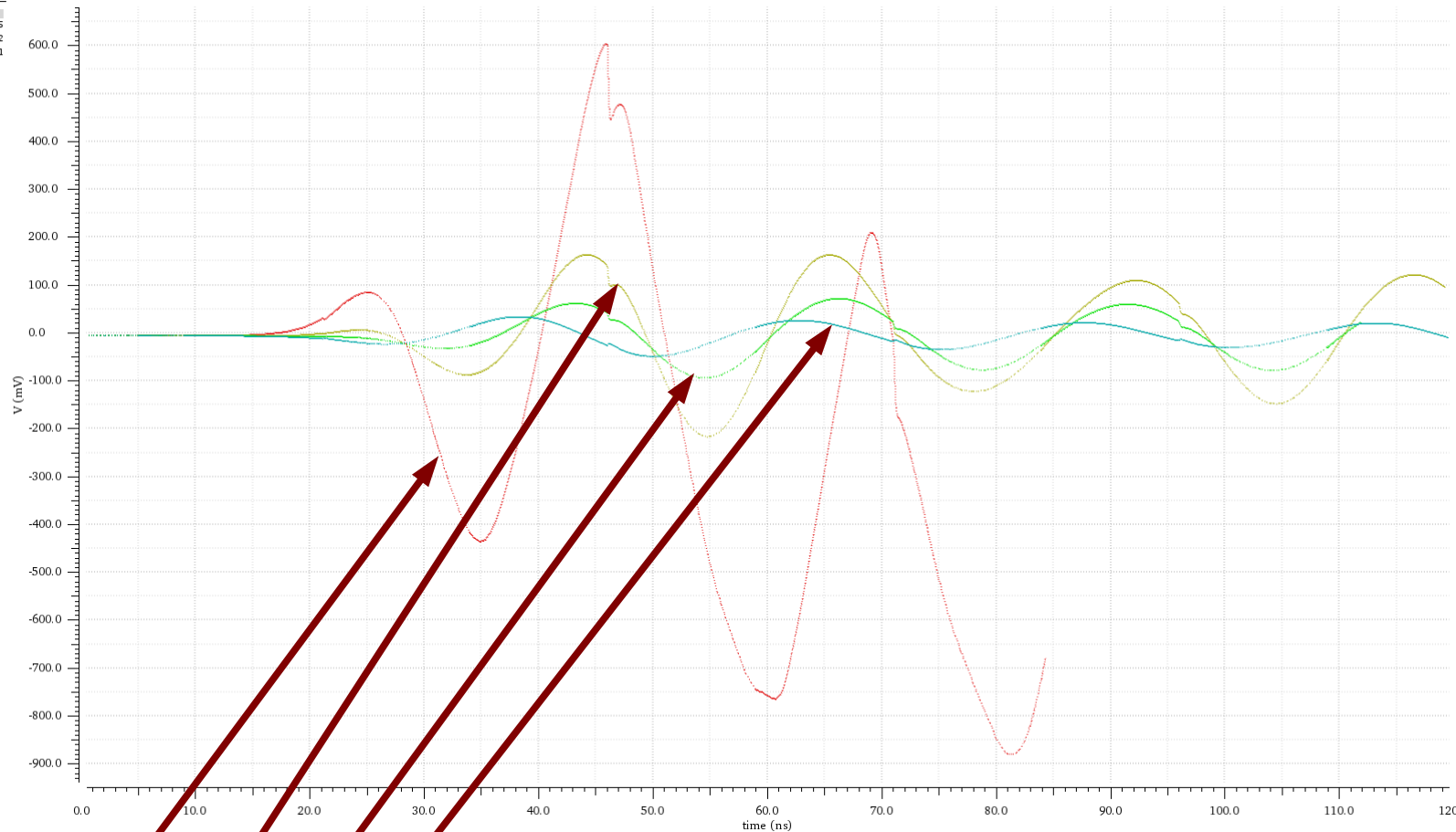
Schematic Simulations of SALT 2.5

S2Diff out (Cdet 12pF)

Vdd_ADC coup. to Vdda and Vdda to Vssa_Pre

Transient Analysis `tran`: time = (0 s -> 150 ns)

Name	bit ...t
/out_s2d<94,93,64:60>	...
/out_s2d<94,93,64:60>	...
/out_s2d<94,93,64:60>	...
/out_s2d<94,93,64:60>	...

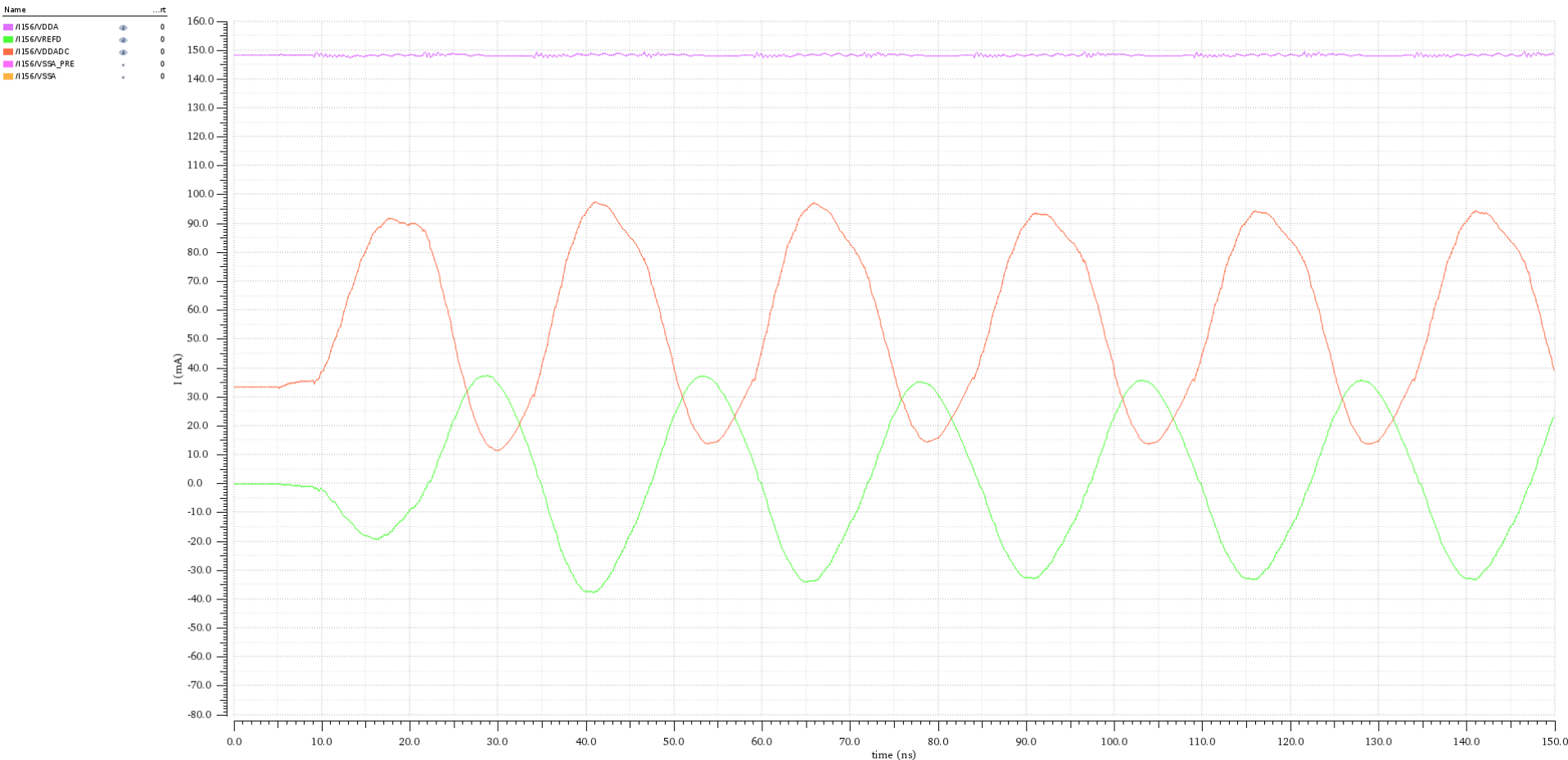


- Lcoupling: -0.5, -0.2, -0.1, 0
- Effect is still there but higher coupling (>0.2) is needed

Simulations of SALT 2.5

Why we see the effects of internal L/Lcouplings while others do not ?

Transient Analysis `tran` time = (0 s -> 150 ns)



- I_{vddadc} current changes by $\sim 90\text{mA}$ during 25ns period !
- Effect of Lcoupling is multiplied by huge current changes !