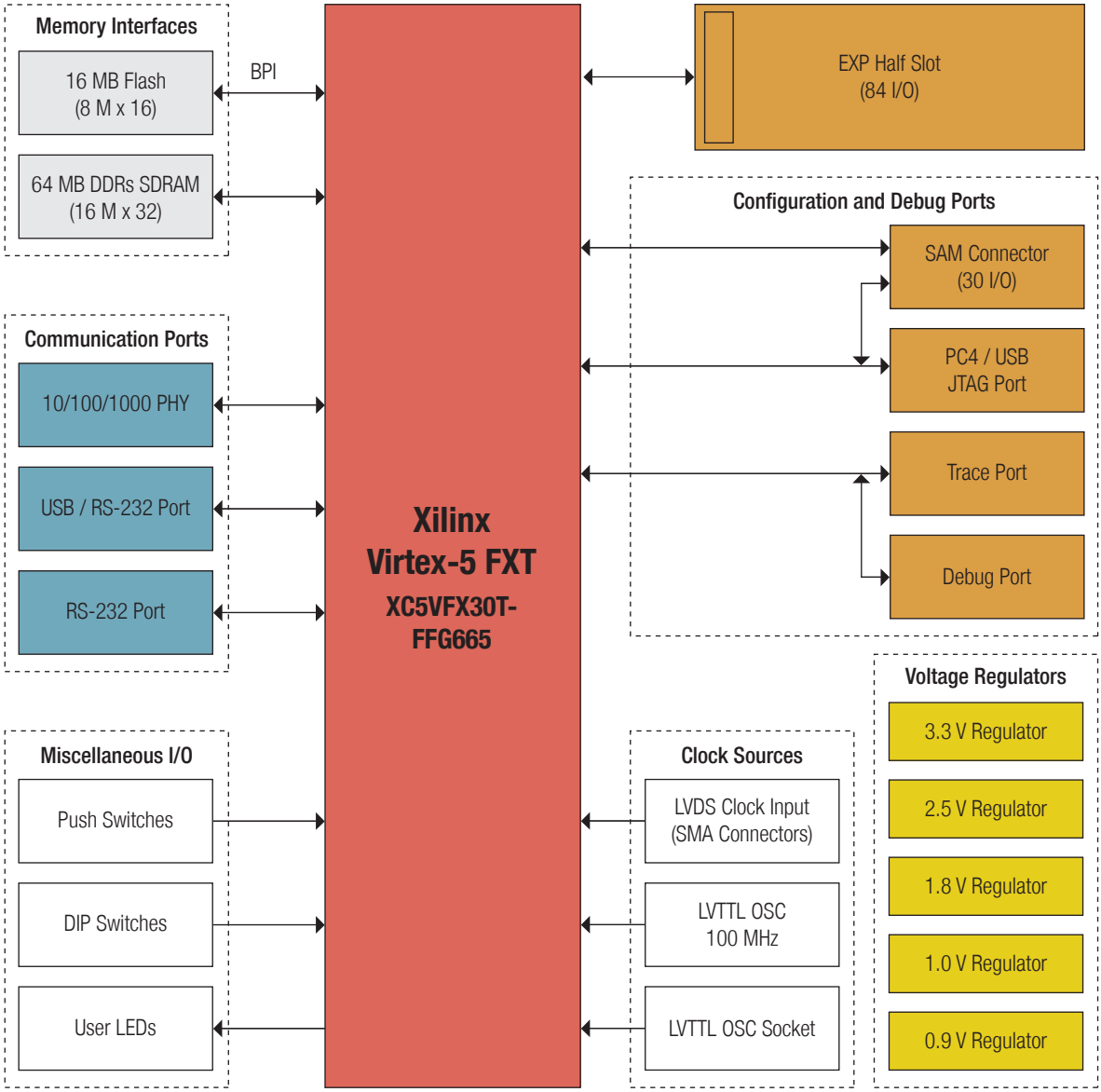
Memory Interfaces
16 MB Flash
(8 M x 16)
64 MB DDRs SDRAM
(16 M x 32)
BPI
Communication Ports
10/100/1000 PHY
USB / RS-232 Port
RS-232 Port
Miscellaneous I/O
Push Switches
DIP Switches
User LEDs
Xilinx
Virtex-5 FXT
XC5VFX30T-
FFG665
EXP Half Slot
(84 I/O)
Configuration and Debug Ports
SAM Connector
(30 I/O)
PC4 / USB
JTAG Port
Trace Port
Debug Port
Clock Sources
LVDS Clock Input
(SMA Connectors)
LVTTL OSC
100 MHz
LVTTL OSC Socket
Voltage Regulators
3.3 V Regulator
2.5 V Regulator
1.8 V Regulator
1.0 V Regulator
0.9 V Regulator