Avnet Product Brief



The Xilinx Virtex-5 FXT Evaluation Kit provides the necessary features for exploring PowerPC 440 based system architectures using the Xilinx Virtex-5 FXT FPGA family. This low cost evaluation platform is ideal for both software and FPGA designers needing an easy to use, entry level system for code development and debug, processor systems prototyping, and general FXT evaluation.

The Virtex-5 FXT system board uses the Xilinx Virtex-5 FX30T-1 FPGA in the FFG665 package. With over 32,000 logic cells, 64 DSP-48 slices, embedded PowerPC 440 core, and built-in 10/100/1000 Ethernet MAC, the FX30T FPGA provides the essential building blocks for system integration needs. The system board includes 64 MB of DDR2 SDRAM, 16 MB of parallel Flash memory, a 10/100/1000 Ethernet PHY device, an RS-232 and USB-to-UART serial port, and an assortment of LEDs and switches. FPGA configuration is accomplished through a JTAG port connector, parallel Flash BPI mode, or through the System ACE interface with an optional System ACE module. PowerPC development is aided through a dedicated debug and TRACE port. An on-board 100 MHz clock oscillator provides the system clock or alternative clocking can be added through the user oscillator socket or an external LVDS compatible clock input port. The half EXP expansion slot supports up to 84 user I/O in both single-ended and differential formats.

Xilinx Virtex-5 FXT Evaluation Kit Includes:

- Evaluation board
- ISE™ WebPACK™ 10.1 DVD
- Wall-mount power supply (5 V)
- Downloadable documentation and reference designs



Xilinx® Virtex™-5 FXT Evaluation Kit

Features:

FPGA

Xilinx XC5VFX30T-1FFG665 Virtex-5 FPGA

I/O Peripherals

- Eight LEDs
- DIP switches
- Four push-button switches
- On-board 100 MHz LVTTL oscillator
- User clock inputs via differential SMA connectors

Board I/O Connectors

- EXP half expansion slot
- System ACE™ module header

Memory

- 64 MB DDR2 SDRAM
- 16 MB Flash

Communication

- RS-232 and USB-UART serial ports
- 10/100/1000 Ethernet PHY

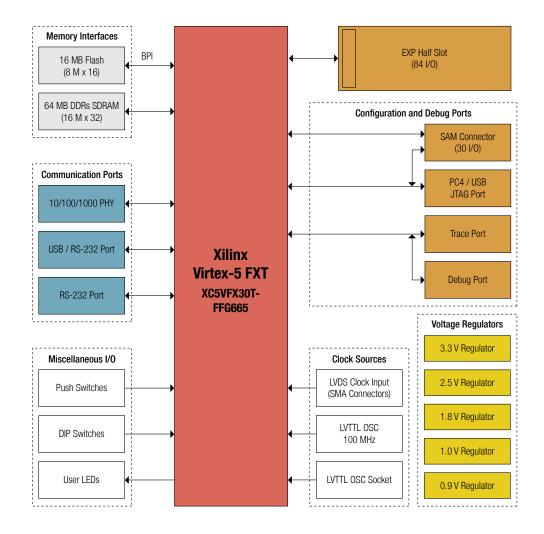
Configuration

- System ACE option
- Xilinx JTAG interface
- BPI Configuration

Target Applications:

- PowerPC™ 440 software development
- General FPGA prototyping
- Communications systems
- Image processing





Featured Manufacturers:















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Ordering Information:

ADS-FOUNDATION-BUNDLE

Part Number AES-V5FXT-EVL30-G

ADS-EDK-BUNDLE

Hardware

Xilinx Virtex-5 FXT Evaluation Kit Xilinx Embedded Development Kit Resale \$395.00 USD \$200.00 USD

(only available with purchase of the above kit)

ISE Foundation

\$2,400.00 USD (only available with purchase of the above kit)

To purchase this kit, visit www.em.avnet.com/virtex5fxt-evl

